IN THE ABSTRACT OF THE DISCLOSURE

Cancel the Abstract, and substitute therefor the following Abstract of the Disclosure.

ABSTRACT

A memory test system can screens objects of tests accurately at low cost using in quasi-operating conditions by utilizing a personal computer (PC). The system utilizes a PC tester having comprising a measurement PC unit that carries a memory module to be used as reference; a signal distribution unit for distributing the signal taken out from the measurement PC unit; a plurality of PFBs performance boards mounted with respective objected-objective products to be observed simultaneously by using the distributed signalsdistributed by the signal distribution unit; a display panel for displaying the current status of the test that is being conducted; a power source for producing the operating voltage of the system; and a control PC for controlling the selection of test parameters and various analytical operations. tester is adapted to takes out the signal from the chip set LSI on the PC mother board in the measurement PC unit to the individual memories on the memory module or the memory module



Serial No. 09/736,282

T&A-104

al

per se <u>for testing</u> and test them in quasi-operating conditions.